## 2SD1010

### Silicon NPN epitaxial planer type

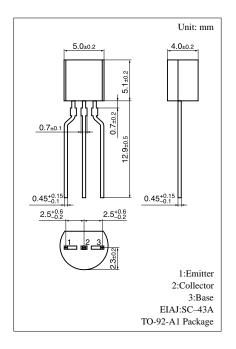
#### For low-frequency amplification

#### Features

- High foward current transfer ratio h<sub>FE</sub>.
- Low collector to emitter saturation voltage V<sub>CE(sat)</sub>.
- High emitter to base voltage V<sub>EBO</sub>.
- Low noise voltage NV.

#### Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	$V_{CBO}$	50	V
Collector to emitter voltage	$V_{CEO}$	40	V
Emitter to base voltage	$V_{\rm EBO}$	15	V
Peak collector current	$I_{CP}$	100	mA
Collector current	$I_{C}$	50	mA
Collector power dissipation	$P_{C}$	300	mW
Junction temperature	$T_{j}$	150	°C
Storage temperature	$T_{\rm stg}$	<b>−55</b> ~ <b>+150</b>	°C



### Electrical Characteristics (Ta=25°C)

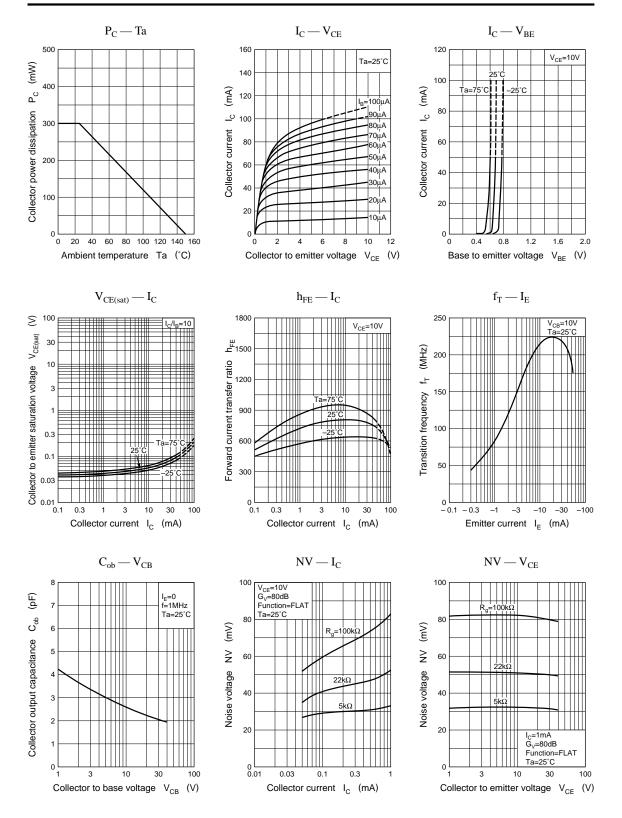
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	$I_{CBO}$	$V_{CB} = 20V, I_E = 0$			100	nA
	I <sub>CEO</sub>	$V_{CE} = 20V, I_B = 0$			1	μА
Collector to base voltage	V <sub>CBO</sub>	$I_{\rm C} = 10 \mu A, I_{\rm E} = 0$	50			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_{C} = 1 \text{mA}, I_{B} = 0$	40			V
Emitter to base voltage	V <sub>EBO</sub>	$I_E = 10 \mu A, I_C = 0$	15			V
Forward current transfer ratio	h <sub>FE</sub> *	$V_{CE} = 10V, I_{C} = 2mA$	400	1000	2000	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{C} = 10\text{mA}, I_{B} = 1\text{mA}$		0.05	0.2	V
Transition frequency	$f_T$	$V_{CB} = 10V$ , $I_E = -2mA$ , $f = 200MHz$		200		MHz
Noise voltage	NV	$V_{CE} = 10V, I_C = 1mA, G_V = 80dB$ $R_g = 100k\Omega, Function = FLAT$		80		mV

#### \*h<sub>FE</sub> Rank classification

Rank	R	S	T
$h_{FE}$	400 ~ 800	600 ~ 1200	1000 ~ 2000

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Transistor 2SD1010



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